FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 30585/6	SERIAL NO. 09/322,443
INFORMATION DISCLOSURE CITATION *(Use several sheets if necessary)*	APPLICANT David L. Reese, et al.		
	FILING DATE May 28, 1999	GROUP ART UNIT 2751	

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Cd		3,544,969	12/01/70	Rakoczi	340	172.5	Nov. 27, 1967
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Cd		4,455,602	06/19/84	Baxter	364	200	May 22, 981
Cd		4,575,797	03/11/86	Gruner	364	200	May 22, 1981
Cd		5,638,525	06/10/97	Hammond	395	385	Feb. 10, 1995
Cd		5,822,578	10/13/98	Frank	395	591	June 5, 1995
Cd		5,832,205	11/03/98	Kelly	395	185.06	Aug. 20, 1996
Cd		5,930,509	07/27/99	Yates	395	707	Jan. 29, 1996
Cd		5,958,047	09/28/99	Panwar	712	237	Jun. 25, 1997

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
		WO 96/24895	08/15/96	PCT/US	G06F	9/22	

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OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Papers, Etc.)*

EXAMINER INITIAL			DOCUMENT
Cd			Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)
Cd			Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)
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OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Papers, Etc.)*

	Cd		Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm
	Cd		Ebcioglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)
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U.S. PATENT DOCUMENTS

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		5,796,939	8/18/98	Berc, et al.	395	184.01	Mar. 10, 1997
		5,802,272	9/1/98	Sites, et al.	395	183.21	Dec. 19, 1994

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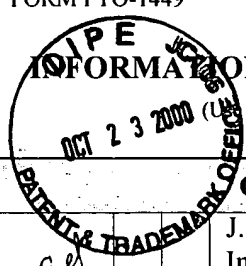| EXAMINER	DATE CONSIDERED

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Cd	4,575,797	03/11/1986	Gruner	364	200	May 22, 1981
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Cd	5,802,373	09/01/1998	Yates	395	705	Jan. 29, 1996
Cd	5,822,578	10/13/1998	Frank	395	591	June 5, 1995
Cd	5,832,205	11/03/1998	Kelly	395	185.06	Aug. 20, 1996
Cd	5,842,017	11/24/1998	Hookway	395	707	Jan. 29, 1996
Cd	5,930,509	07/27/1999	Yates	395	707	Jan. 29, 1996
Cd	5,949,415	09/07/1999	Lin	345	335	Jun. 16, 1997
Cd	5,958,047	09/28/1999	Panwar	712	237	Jun. 25, 1997
Cd	6,000,028	12/07/1999	Chernoff	712	226	Jan. 29, 1996
Cd	6,091,897	07/18/2000	Yates	395	708	Jan. 29, 1996

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Papers, Etc.)*

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Cd		Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)
Cd		Anton Chernoff, et al., FX!32, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)
Cd		Compaq Computer Corp., Compiler Writer's Guide for the Alpha 21264, 1999
Cd		T.M. Conte, et al., "Accurate and practical profile-driven compilation using the profile buffer," Proceedings of the 29th Annual International Symposium on Microarchitecture, Paris, France, pp. 36-45 (Dec. 1996)
Cd		T.M. Conte, et al., "Hardware based profiling: An effective technique for profile-driven optimization," International Journal of Parallel Programming, vol. 24, no. 2 (Feb. 1996)
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C.d		J.S. Cox et al., "Commercializing profile-driven optimization." Proceedings of the 28th Hawaii International Conference on System Sciences, vol. 1 Maui, HI, pp. 221-228 (Jan. 1995)
C.d		Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm
C.d		Ebcioglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)
C.d		Linda Geppert et al., Transmeta's Magic Show, IEEE Spectrum, vol. 37 no. 5, pp. 26-33 (May 2000)
C.d		Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)
C.d		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)
C.d		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)
C.d		Linley Gwennap, First Merced Patent Surfaces, Intel Document Reveals Processors With Dual Instruction Sets, Microprocessor Report, MicroDesign Resources (July 20, 1998)
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